

In re the Application of: M. Powell et al.

Serial No.: 08/887,680

Filed: July 3, 1997
 OCT 21 1997

METHOD AND APPARATUS FOR EXTENDING COMPUTER ARCHITECTURE FROM 32 TO 64 BITS

THE HONORABLE COMMISSIONER OF PATENTS AND TRADEMARKS
 Washington, D.C. 20231

Sir:

Transmitted herewith is an Preliminary Amendment in the above-identified application.

Small entity status of this Application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
 A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
 XXX No additional fee is required.

The fee has been calculated as shown below:

	(Col. 1) CLAIMS REMAINING AFTER AMD.		(Col. 2) HIGHEST NO. PREVIOUSLY PAID FOR	(Col. 3) PRESENT EXTRA	SMALL ENTITY RATE	ADDITIONAL FEE	OTHER THAN A SMALL ENTITY RATE	ADDITIONAL FEE
Total claims:	38	minus	33	5	x \$11.00 =		x \$22.00 =	\$110.00
Indep. Claims:	5	minus	5	0	x \$41.00 =		x \$82.00 =	
<input type="checkbox"/> First Presentation of Multiple Dependent Claim(s)					+ 135.00 =		+ 270.00 =	
** If the difference in (Col. 2) is less than zero, enter "0" in (Col. 3)					Total Additional:		Total Additional:	\$110.00

Applicant hereby Petitions for an Extension of Time of __ month(s), pursuant to Rule 1.136(a).
 A check in the amount of \$_____ is attached for processing fees under 37 CFR 1.17.
 Please charge my Deposit Account No. 02-2666 the amount of \$_____.
 A duplicate copy of this sheet is enclosed.
 A check in the amount of \$110.00 is attached for presentation of additional claim(s).
 XXX The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 02-2666:
 XXX Any additional filing fees required under 37 CFR 1.16 for presentation of extra claims.
 XXX Any extension or petition fees under 37 CFR 1.17.
 A duplicate copy of this sheet is enclosed.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: 10/26/97

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Our Ref. No.: 82225.P0189R
EXPRESS MAIL NO. EM560650761US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

M. Powell et al.

Serial No.: 08/887,680

Filed: July 3, 1997

For: **METHOD AND APPARATUS FOR
EXTENDING COMPUTER
ARCHITECTURE FROM 32 TO 64 BITS**

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Please amend the above-identified application as follows:

IN THE CLAIMS

1 ~~33. (Amended) A method for context-switching a processor that executes procedures having~~
2 ~~differing word sizes, comprising the steps of:~~
3 ~~testing a least significant bit of a stack pointer register in the processor that indicates whether a set~~
4 ~~of data values for a procedure that are stored in a set of registers in the processor, each have a~~
5 ~~first word size or a second word size, wherein the first word size is less than the second word~~
6 ~~size;~~
7 ~~transferring the data values from a least significant portion of each register to a first stack save area~~
8 ~~in memory if the least significant bit of the stack pointer register indicates the first word size;~~
9 ~~transferring the data values from the registers to a second stack save area in memory if the least~~
10 ~~significant bit of the stack pointer register indicates the second word size[;~~
11 ~~setting a width indication bit in the first stack save area in memory, and transferring the data values~~
12 ~~from the registers to a second stack save area in memory and transferring the stack pointer value~~